EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
		shift adj register$1 and multiple and stages and connected first and stage and start and signal and coupled and input and terminal and shift and register and sequentially and outputting and output and signals and respective and stages and multiple and stages and including and odd and stages and receiving and first and clock and signal and even and stages and receiving and second and clock and signal and	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/28 12:28
L1	0	"1020000050311"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/20 09:49
L2	7	Anyang-si same jeon same jin	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/20 10:15
L3	0	Anyang-si same jeon same jin and shift same registe	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/20 10:16
L4	7	Anyang-si same jeon same jin	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/20 10:16
L5	6	Anyang-si same jeon same jin and shift same register	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/20 10:16
L6	0	mun same seung same hwan	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/20 10:45
L7	133	moon same seung same hwan	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/20 10:45

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L8	35	moon same seung same hwan and shift same register and "345"/$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/20 10:46
L9	16	moon same seung same hwan and shift same register and "345"/$.ccls. and pull adj up	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/20 10:46
L10	16	moon same seung same hwan and shift same register and "345"/$.ccls. and pull adj up and pull adj down	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/20 10:47
L11	14	moon same seung same hwan and shift same register and "345"/$.ccls. and pull adj up and pull adj down and provid$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/20 10:58
L12	0	moon same seung same hwan and shift same register and "345"/$.ccls. and pull adj up and pull adj down and provid$6 same two same clok$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/20 10:47
L13	0	moon same seung same hwan and shift same register and "345"/$.ccls. and pull adj up and pull adj down and provid$6 same two same clock$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/20 10:48
L14	7	moon same seung same hwan and shift same register and "345"/$.ccls. and pull adj up and pull adj down and provid$6 and two same clock$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/20 10:48
L15	6	moon same seung same hwan and shift same register and "345"/$.ccls. and pull adj up and pull adj down and provid$6 and two same clock$6 and ris$6 same edge	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/20 10:48
L16	3	moon same seung same hwan and shift same register and "345"/$.ccls. and pull adj up and pull adj down and provid$6 and two same clock$6 and ris$6 same edge same input$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/20 10:49

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L17	3	moon same seung same hwan and shift same register and "345"/$.ccls. and pull adj up and pull adj down and provid$6 and two same clock$6 and ris$6 same edge same input$6 same output$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/20 10:49
L18	3	moon same seung same hwan and shift same register and "345"/$.ccls. and pull adj up and pull adj down and provid$6 and two same clock$6 and ris$6 same edge same input$6 same output$6 and turn$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/20 10:49
L19	0	moon same seung same hwan and shift same register and "345"/$.ccls. and pull adj up and pull adj down and provid$6 and two same clock$6 and ris$6 same edge same input$6 same output$6 and turn$6 and on/off	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/20 10:50
L20	3	moon same seung same hwan and shift same register and "345"/$.ccls. and pull adj up and pull adj down and provid$6 and two same clock$6 and ris$6 same edge same input$6 same output$6 and turn$6 and (on/off or pull-up or pull-down)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/20 10:51
L21	2	"6690347".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/20 11:06
S1	42168	display$6 and cell$1 same array$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/08 09:07
S2	11136	display$6 and cell$1 same array$6 and data same driv$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/08 09:07
S3	3532	display$6 and cell$1 same array$6 and data same driv$6 and gate same driv$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/08 09:07
S4	0	display$6 and cell$1 same array$6 and data same driv$6 and gate same driv$6 and substarte	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/08 09:08

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S5	2197	display$6 and cell$1 same array$6 and data same driv$6 and gate same driv$6 and substrate	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/08 09:08
S6	2197	display$6 and cell$1 same array$6 and data same driv$6 and gate same driv$6 and substrate$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/08 09:08
S7	264	display$6 and cell$1 same array$6 and data same driv$6 and gate same driv$6 and substrate$1 and gate same driv$6 same shift adj register$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/08 09:10
S8	16	display$6 and cell$1 same array$6 and data same driv$6 and gate same driv$6 and substrate$1 and gate same driv$6 same shift adj register$6 same cascade$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/08 09:31
S9	0	display$6 and cell$1 same array$6 and data same driv$6 and gate same driv$6 and substrate$1 and gate same driv$6 same shift adj register$6 same cascade$6 same odd same reciev$6 same clock$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/08 09:11
S10	3	display$6 and cell$1 same array$6 and data same driv$6 and gate same driv$6 and substrate$1 and gate same driv$6 same shift adj register$6 same cascade$6 same odd same receiv$6 same clock$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/08 09:11
S11	10	display$6 and cell$1 same array$6 and data same driv$6 and gate same driv$6 and substrate$1 and gate same driv$6 same shift adj register$6 same cascade$6 and pull same down same up	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/08 09:32
S12	16	display$6 and cell$1 same array$6 and data same driv$6 and gate same driv$6 and substrate$1 and gate same driv$6 same shift adj register$6 same cascade$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/08 09:55
S13	705	jin and jeon	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/08 09:34

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S14	31	jin and jeon and shift adj register$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/08 09:34
S15	20	jin and jeon and shift adj register$6 and display	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/08 09:34
S16	12	jin and jeon and shift adj register$6 and display and pull-up and pull-down	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/08 09:35
S17	6	jin and jeon and shift adj register$6 and display and pull-up and pull-down and hyung and guel	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/08 09:36
S18	4	jin and jeon and shift adj register$6 and display and pull-up and pull-down and hyung and guel and seung and hwan and moon	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/08 09:36
S19	1443	345/100.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/08 09:38
S20	1417	345/100.ccls. and display	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/08 09:38
S21	844	345/100.ccls. and display and shift adj register$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/08 09:38
S22	30	345/100.ccls. and display and shift adj register$6 and pull-up and pull-down	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/08 09:38
S23	24	345/100.ccls. and display and shift adj register$6 and pull-up and pull-down and capacitor$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/08 09:47

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S24	24	345/100.ccls. and display and shift adj register$6 and pull-up and pull-down and capacitor$6 and clock$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/08 09:47
S25	15	345/100.ccls. and display and shift adj register$6 and pull-up and pull-down and capacitor$6 and clock$6 and odd and even	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/08 09:52
S26	8	345/100.ccls. and display and shift adj register$6 and cascade$6 and pull-up and pull-down and capacitor$6 and clock$6 and odd and even	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/08 10:02
S27	11	345/88-100.ccls. and display and shift adj register$6 and cascade$6 and pull-up and pull-down and capacitor$6 and clock$6 and odd and even	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/08 10:15
S28	14	(345/88-100.ccls. or 315/169.1-.3 or 340/666 or 340/784 or 349/149 or 377/78 or 377/64) and display and shift adj register$6 and cascade$6 and pull-up and pull-down and capacitor$6 and clock$6 and odd and even	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/08 10:17
S29	4129	345/100-105.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/27 16:39
S30	17037	shift adj register$1 and multiple and stages and connected first and stage and start and signal and coupled and input and terminal and shift and register and sequentially and outputting and output and signals and respective and stages and multiple and stages and including and odd and stages and receiving and first and clock and signal and even and stages and receiving and second and clock and signal and phase and opposite and first and clock and signal and each and multiple and stages and pull-up and means and providing and corresponding and first and second and clock	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/28 12:29

EAST Search History

S31	0	shift adj register$1 and multiple and stages and connected and first and stage and start and signal and coupled and input and terminal and shift and register and sequentially and outputting and output and signals and respective and stages and multiple and stages and including and odd and stages and receiving and first and clock and signal and even and stages and receiving and second and clock and signal and phase and opposite and first and clock and signal and each and multiple and stages and pull-up and means and providing and corresponding and first and second and clock	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/28 12:30
S32	2387	shift adj register$1 and multiple and stages and connected and first and stage and start and signal and coupled and input and terminal and shift and register and sequentially	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/28 12:30
S33	451	shift adj register$1 and multiple and stages and connected and first and stage and start and signal and coupled and input and terminal and shift and register and sequentially and outputting and output and signals and respective and stages and multiple and stages and including and odd	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/28 12:31
S34	403	shift adj register$1 and multiple and stages and connected and first and stage and start and signal and coupled and input and terminal and shift and register and sequentially and outputting and output and signals and respective and stages and multiple. and stages and including and odd and stages and receiving and first and clock and signal and even	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/28 12:31
S35	240	shift adj register$1 and multiple and stages and connected and first and stage and start and signal and coupled and input and terminal and shift and register and sequentially and outputting and output and signals and respective and stages and multiple and stages and including and odd and stages and receiving and first and clock and signal and even and stages and receiving and second and clock and signal and phase and opposite and first and clock and signal	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/28 12:31

EAST Search History

S36	0	shift adj register$1 and multiple and stages and connected and first and stage and start and signal and coupled and input and terminal and shift and register and sequentially and outputting and output and signals and respective and stages and multiple and stages and including and odd and stages and receiving and first and clock and signal and even and stages and receiving and second and clock and signal and phase and opposite and first and clock and signal and each and multiple and stages and pull-up and means and providing and corresponding and first and second and clock	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/28 12:32
S37	0	shift adj register$1 and multiple and stages and connected and first and stage and start and signal and coupled and input and terminal and shift and register and sequentially and outputting and output and signals and respective and stages and multiple and stages and including and odd and stages and receiving and first and clock and signal and even and stages and receiving and second and clock and signal and phase and opposite and first and clock and signal and each and multiple and stages and pull-up and means	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/28 12:33
S38	132	shift adj register$1 and multiple and stages and connected and first and stage and start and signal and coupled and input and terminal and shift and register and sequentially and outputting and output and signals and respective and stages and multiple and stages and including and odd and stages and receiving and first and clock and signal and even and stages and receiving and second and clock and signal and phase and opposite and first and clock and signal and multiple and stages and pull-up and means	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/28 12:33

EAST Search History

S39	132	shift adj register$1 and multiple and stages and connected and first and stage and start and signal and coupled and input and terminal and shift and register and sequentially and outputting and output and signals and respective and stages and multiple and stages and including and odd and stages and receiving and first and clock and signal and even and stages and receiving and second and clock and signal and phase and opposite and first and clock and signal and multiple and stages and pull-up and means and (each or individual)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/28 12:34
S40	0	shift adj register$1 and multiple and stages and connected and first and stage and start and signal and coupled and input and terminal and shift and register and sequentially and outputting and output and signals and respective and stages and multiple and stages and including and odd and stages and receiving and first and clock and signal and even and stages and receiving and second and clock and signal and phase and opposite and first and clock and signal and multiple and stages and pull-up and means and (each or individual) and providing and corresponding and first and second and clock and signals and output and terminal and pull-up and driving and means and connected and input	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/28 12:34

EAST Search History

S41	132	shift adj register$1 and multiple and stages and connected and first and stage and start and signal and coupled and input and terminal and shift and register and sequentially and outputting and output and signals and respective and stages and multiple and stages and including and odd and stages and receiving and first and clock and signal and even and stages and receiving and second and clock and signal and phase and opposite and first and clock and signal and multiple and stages and pull-up and means and (each or individual) and providing and correspond$6 and first and second and clock and signals and output and terminal and pull-up and driving and means and connected and input	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/28 12:35
S42	66	shift adj register$1 and multiple and stages and connected and first and stage and start and signal and coupled and input and terminal and shift and register and sequentially and outputting and output and signals and respective and stages and multiple and stages and including and odd and stages and receiving and first and clock and signal and even and stages and receiving and second and clock and signal and phase and opposite and first and clock and signal and multiple and stages and pull-up and means and (each or individual) and providing and correspond$6 and first and second and clock and signals and output and terminal and pull-up and driving and means and connected and input and node and pull-up and means and turning and pull-up and means and response and front and edge and input and signal and turning and off and pull-up and means and response and front and edge and output and signal and next and stage and pull-down	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/28 12:35

EAST Search History

S43	66	shift adj register$1 and multiple and stages and connected and first and stage and start and signal and coupled and input and terminal and shift and register and sequentially and outputting and output and signals and respective and stages and multiple and stages and including and odd and stages and receiving and first and clock and signal and even and stages and receiving and second and clock and signal and phase and opposite and first and clock and signal and multiple and stages and pull-up and means and (each or individual) and providing and correspond$6 and first and second and clock and signals and output and terminal and pull-up and driving and means and connected and input and node and pull-up and means and turning and pull-up and means and response and front and edge and input and signal and turning and off and pull-up and means and response and front and edge and output and signal and next and stage and pull-down and means and providing and first and power and voltage and output and terminal and pull-down and driving and means and connected and input and node and pull-down and means and turning and off and pull-down and means and response and front and edge and input and signal and turning and pull-down and means and response and front and edge and output and signal and next and stage	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/28 12:35

EAST Search History

S44	0	shift adj register$1 and multiple and stages and connected and first and stage and start and signal and coupled and input and terminal and shift and register and sequentially and outputting and output and signals and respective and stages and multiple and stages and including and odd and stages and receiving and first and clock and signal and even and stages and receiving and second and clock and signal and phase and opposite and first and clock and signal and multiple and stages and pull-up and means and (each or individual) and providing and correspond$6 and first and second and clock and signals and output and terminal and pull-up and driving and means and connected and input and node and pull-up and means and turning and pull-up and means and response and front and edge and input and signal and turning and off and pull-up and means and response and front and edge and output and signal and next and stage and pull-down and means and providing and first and power and voltage and output and terminal and pull-down and driving and means and connected and input and node and pull-down and means and turning and off and pull-down and means and response and front and edge and input and signal and turning and pull-down and means and response and front and edge and output and signal and next and stage and "345"/$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/28 12:37

EAST Search History

S45	27	shift adj register$1 and multiple and stages and connected and first and stage and start and signal and coupled and input and terminal and shift and register and sequentially and outputting and output and signals and respective and stages and multiple and stages and including and odd and stages and receiving and first and clock and signal and even and stages and receiving and second and clock and signal and phase and opposite and first and clock and signal and multiple and stages and pull-up and means and (each or individual) and providing and correspond$6 and first and second and clock and signals and output and terminal and pull-up and driving and means and connected and input and node and pull-up and means and turning and pull-up and means and response and front and edge and input and signal and turning and off and pull-up and means and response and front and edge and output and signal and next and stage and pull-down and means and providing and first and power and voltage and output and terminal and pull-down and driving and means and connected and input and node and pull-down and means and turning and off and pull-down and means and response and front and edge and input and signal and turning and pull-down and means and response and front and edge and output and signal and next and stage and (LCD or liquid adj crystal adj display)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/28 12:38

EAST Search History

S46	0	shift adj register$1 and multiple same stages and connected and first and stage and start and signal and coupled and input and terminal and shift and register and sequentially and outputting and output and signals and respective and stages and multiple and stages and including and odd and stages and receiving and first and clock and signal and even and stages and receiving and second and clock and signal and phase and opposite and first and clock and signal and multiple and stages and pull-up and means and (each or individual) and providing and correspond$6 and first and second and clock and signals and output and terminal and pull-up and driving and means and connected and input and node and pull-up and means and turning and pull-up and means and response and front and edge and input and signal and turning and off and pull-up and means and response and front and edge and output and signal and next and stage and pull-down and means and providing and first and power and voltage and output and terminal and pull-down and driving and means and connected and input and node and pull-down and means and turning and off and pull-down and means and response and front and edge and input and signal and turning and pull-down and means and response and front and edge and output and signal and next and stage and (LCD or liquid adj crystal adj display)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/28 12:38

EAST Search History

S47	38	shift adj register$1 and multiple same stage$1 and connected and first and stage and start and signal and coupled and input and terminal and shift and register and sequentially and outputting and output and signals and respective and stages and multiple and stages and including and odd and stages and receiving and first and clock and signal and even and stages and receiving and second and clock and signal and phase and opposite and first and clock and signal and multiple and stages and pull-up and means and (each or individual) and providing and correspond$6 and first and second and clock and signals and output and terminal and pull-up and driving and means and connected and input and node and pull-up and means and turning and pull-up and means and response and front and edge and input and signal and turning and off and pull-up and means and response and front and edge and output and signal and next and stage and pull-down and means and providing and first and power and voltage and output and terminal and pull-down and driving and means and connected and input and node and pull-down and means and turning and off and pull-down and means and response and front and edge and input and signal and turning and pull-down and means and response and front and edge and output and signal and next and stage	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/28 12:39

EAST Search History

S48	38	shift adj register$1 and multiple same stage$1 and connected and first same stage and start and signal and coupled and input and terminal and shift and register and sequentially and outputting and output and signals and respective and stages and multiple and stages and including and odd and stages and receiving and first and clock and signal and even and stages and receiving and second and clock and signal and phase and opposite and first and clock and signal and multiple and stages and pull-up and means and (each or individual) and providing and correspond$6 and first and second and clock and signals and output and terminal and pull-up and driving and means and connected and input and node and pull-up and means and turning and pull-up and means and response and front and edge and input and signal and turning and off and pull-up and means and response and front and edge and output and signal and next and stage and pull-down and means and providing and first and power and voltage and output and terminal and pull-down and driving and means and connected and input and node and pull-down and means and turning and off and pull-down and means and response and front and edge and input and signal and turning and pull-down and means and response and front and edge and output and signal and next and stage	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/28 12:39

EAST Search History

S49	38	shift adj register$1 and multiple same stage$1 and connected and first same stage and start same signal and coupled and input and terminal and shift and register and sequentially and outputting and output and signals and respective and stages and multiple and stages and including and odd and stages and receiving and first and clock and signal and even and stages and receiving and second and clock and signal and phase and opposite and first and clock and signal and multiple and stages and pull-up and means and (each or individual) and providing and correspond$6 and first and second and clock and signals and output and terminal and pull-up and driving and means and connected and input and node and pull-up and means and turning and pull-up and means and response and front and edge and input and signal and turning and off and pull-up and means and response and front and edge and output and signal and next and stage and pull-down and means and providing and first and power and voltage and output and terminal and pull-down and driving and means and connected and input and node and pull-down and means and turning and off and pull-down and means and response and front and edge and input and signal and turning and pull-down and means and response and front and edge and output and signal and next and stage	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/28 12:39

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S50	0	shift adj register$1 and multiple same stage$1 and connected and first same stage and start same signal same coupled and input and terminal and shift and register and sequentially and outputting and output and signals and respective and stages and multiple and stages and including and odd and stages and receiving and first and clock and signal and even and stages and receiving and second and clock and signal and phase and opposite and first and clock and signal and multiple and stages and pull-up and means and (each or individual) and providing and correspond$6 and first and second and clock and signals and output and terminal and pull-up and driving and means and connected and input and node and pull-up and means and turning and pull-up and means and response and front and edge and input and signal and turning and off and pull-up and means and response and front and edge and output and signal and next and stage and pull-down and means and providing and first and power and voltage and output and terminal and pull-down and driving and means and connected and input and node and pull-down and means and turning and off and pull-down and means and response and front and edge and input and signal and turning and pull-down and means and response and front and edge and output and signal and next and stage	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/28 12:40
S51	44	shift adj register$1 and multiple same stage$1 and connected and first same stage and start same signal same coupled and input and terminal and shift and register and sequentially	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/28 13:46
S52	32	shift adj register$1 and multiple same stage$1 and connected and first same stage and start same signal same coupled same input and terminal and shift and register and sequentially	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/28 12:40
S53	11	shift adj register$1 and multiple same stage$1 and connected and first same stage and start same signal same coupled same input same terminal and shift and register and sequentially	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/28 12:47

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S54	11	shift adj register$1 and multiple same stage$1 and connected and first same stage and start same signal same coupled same input same terminal and shift adj register and sequentially	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/28 12:41
S55	9	shift adj register$1 and multiple same stage$1 and connected and first same stage and start same signal same coupled same input same terminal and shift adj register same sequentially	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/28 12:47
S56	5	shift adj register$1 and multiple same stage$1 and connected and first same stage and start same signal same coupled same input same terminal and shift adj register same sequentially same outputting	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/28 12:41

EAST Search History

S57	0	shift adj register$1 and multiple same stage$1 and connected and first same stage and start same signal same coupled same input same terminal and shift adj register same sequentially same outputting and output same signals and respective and stages and multiple and stages and including and odd and stages and receiving and first and clock and signal and even and stages and receiving and second and clock and signal and phase and opposite and first and clock and signal and multiple and stages and pull-up and means and (each or individual) and providing and correspond$6 and first and second and clock and signals and output and terminal and pull-up and driving and means and connected and input and node and pull-up and means and turning and pull-up and means and response and front and edge and input and signal and turning and off and pull-up and means and response and front and edge and output and signal and next and stage and pull-down and means and providing and first and power and voltage and output and terminal and pull-down and driving and means and connected and input and node and pull-down and means and turning and off and pull-down and means and response and front and edge and input and signal and turning and pull-down and means and response and front and edge and output and signal and next and stage	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/28 12:42
S58	5	shift adj register$1 and multiple same stage$1 and connected and first same stage and start same signal same coupled same input same terminal and shift adj register same sequentially same outputting and output same signals and respective and stages	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/28 12:42
S59	5	shift adj register$1 and multiple same stage$1 and connected and first same stage and start same signal same coupled same input same terminal and shift adj register same sequentially same outputting and output same signals and respective same stages	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/28 12:42

EAST Search History

S60	5	shift adj register$1 and multiple same stage$1 and connected and first same stage and start same signal same coupled same input same terminal and shift adj register same sequentially and pull-up	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/28 12:47
S61	7	shift adj register$1 and multiple same stage$1 and connected and first same stage and start same signal same coupled and input and terminal and shift and register and sequentially and "345"/$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/28 13:44
S62	8	shift adj register$1 and multiple same stage$1 and connected and first same stage and start same signal same coupled and input and terminal and shift and register and sequentially and pull-up	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/28 13:46
S63	5	shift adj register$1 and multiple same stage$1 and connected and first same stage and start same signal same coupled and input and terminal and shift and register and sequentially and pull-up and pull-down	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/28 13:46